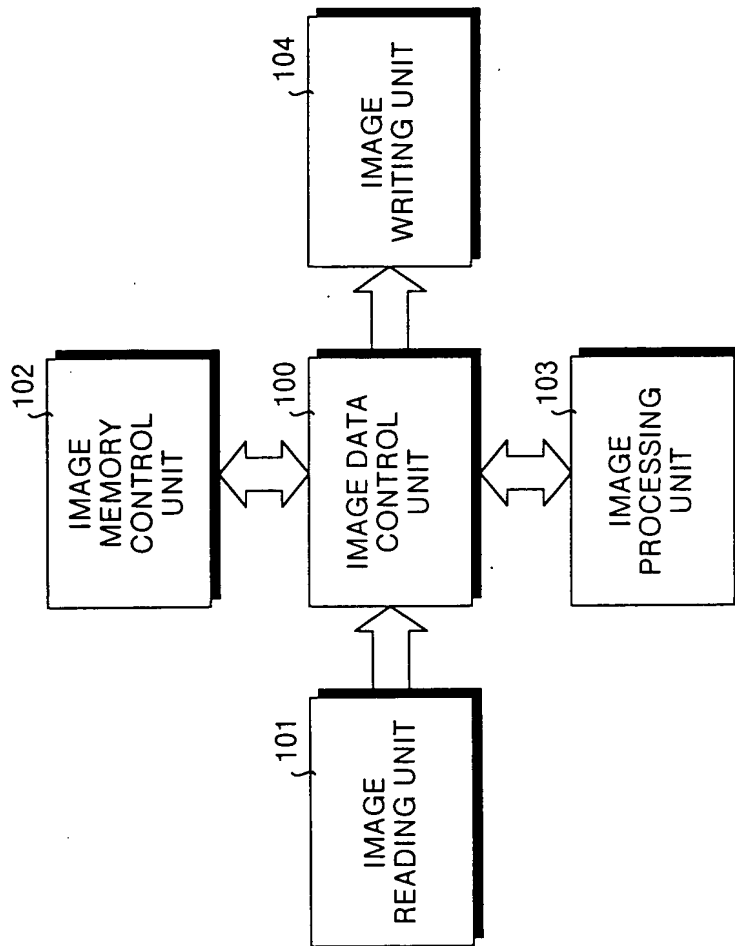
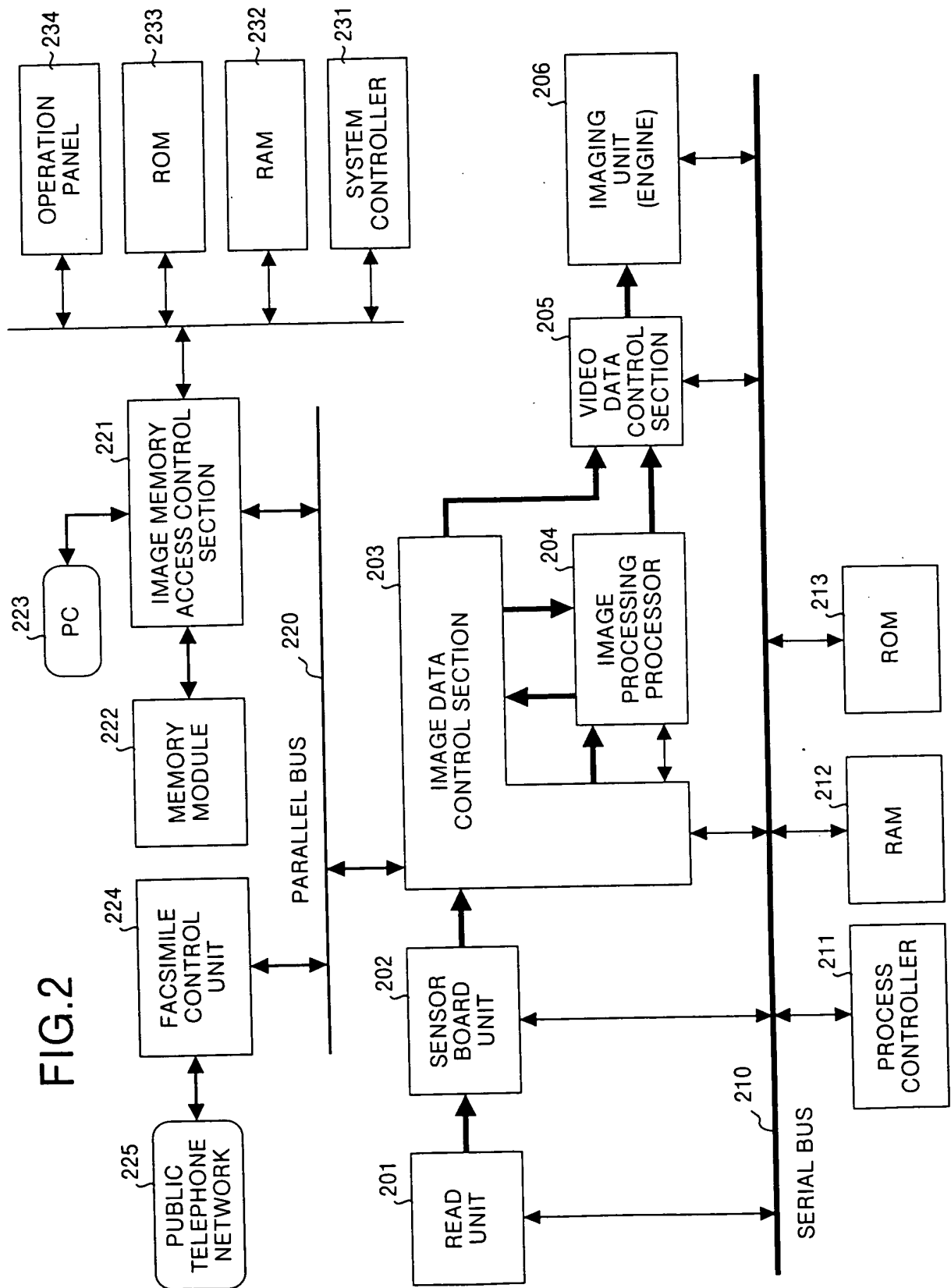


FIG.1

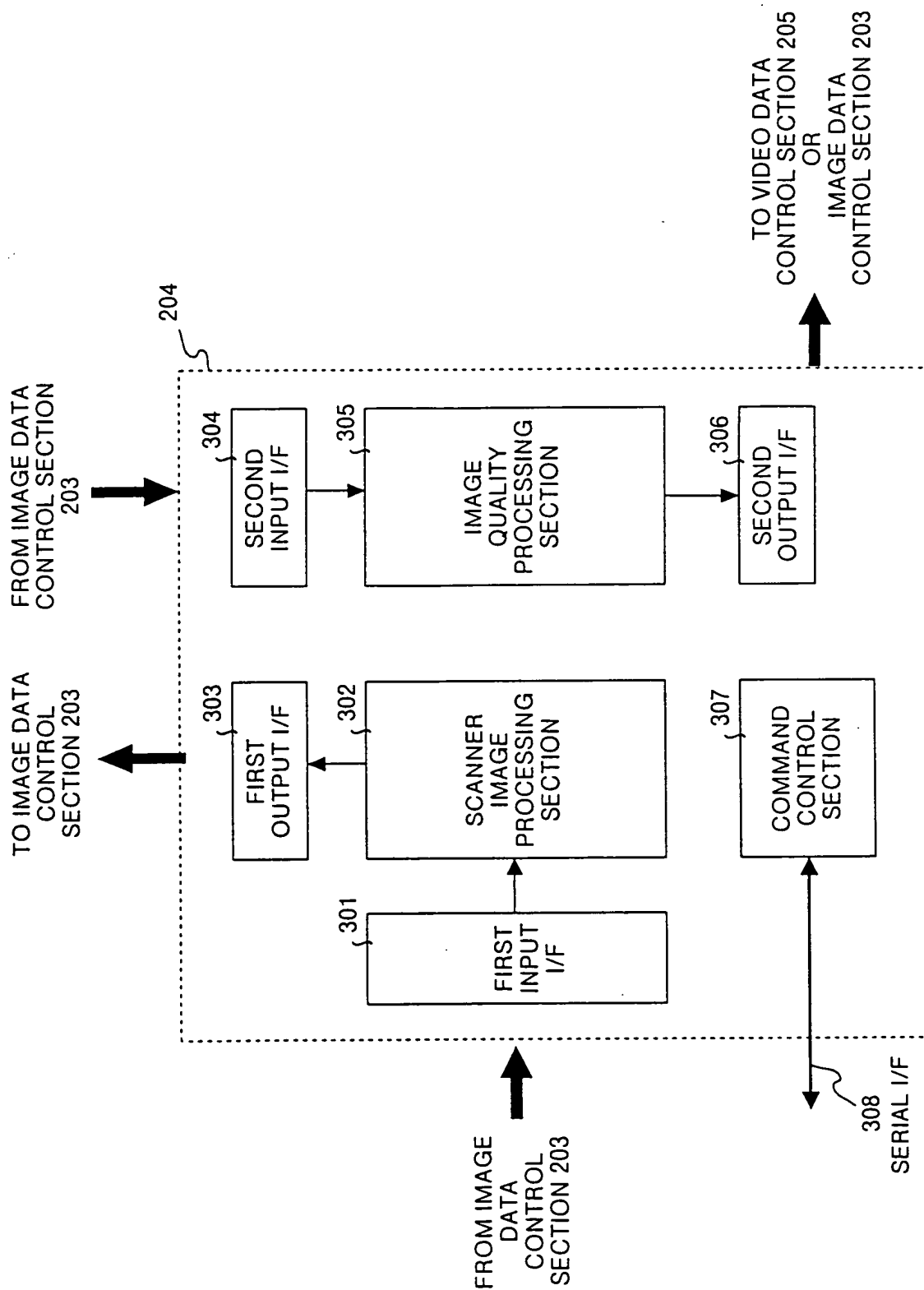


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FIG.3



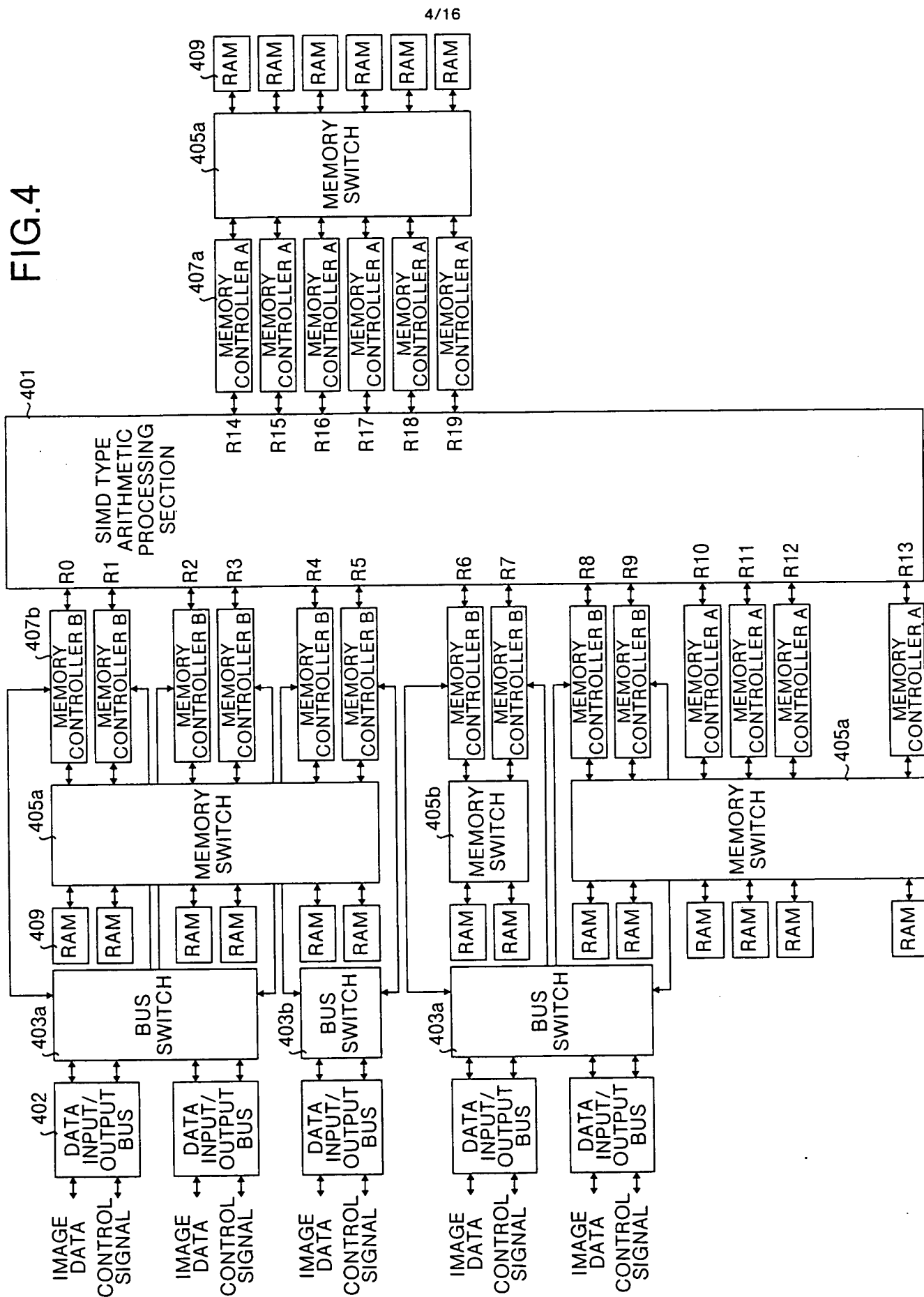
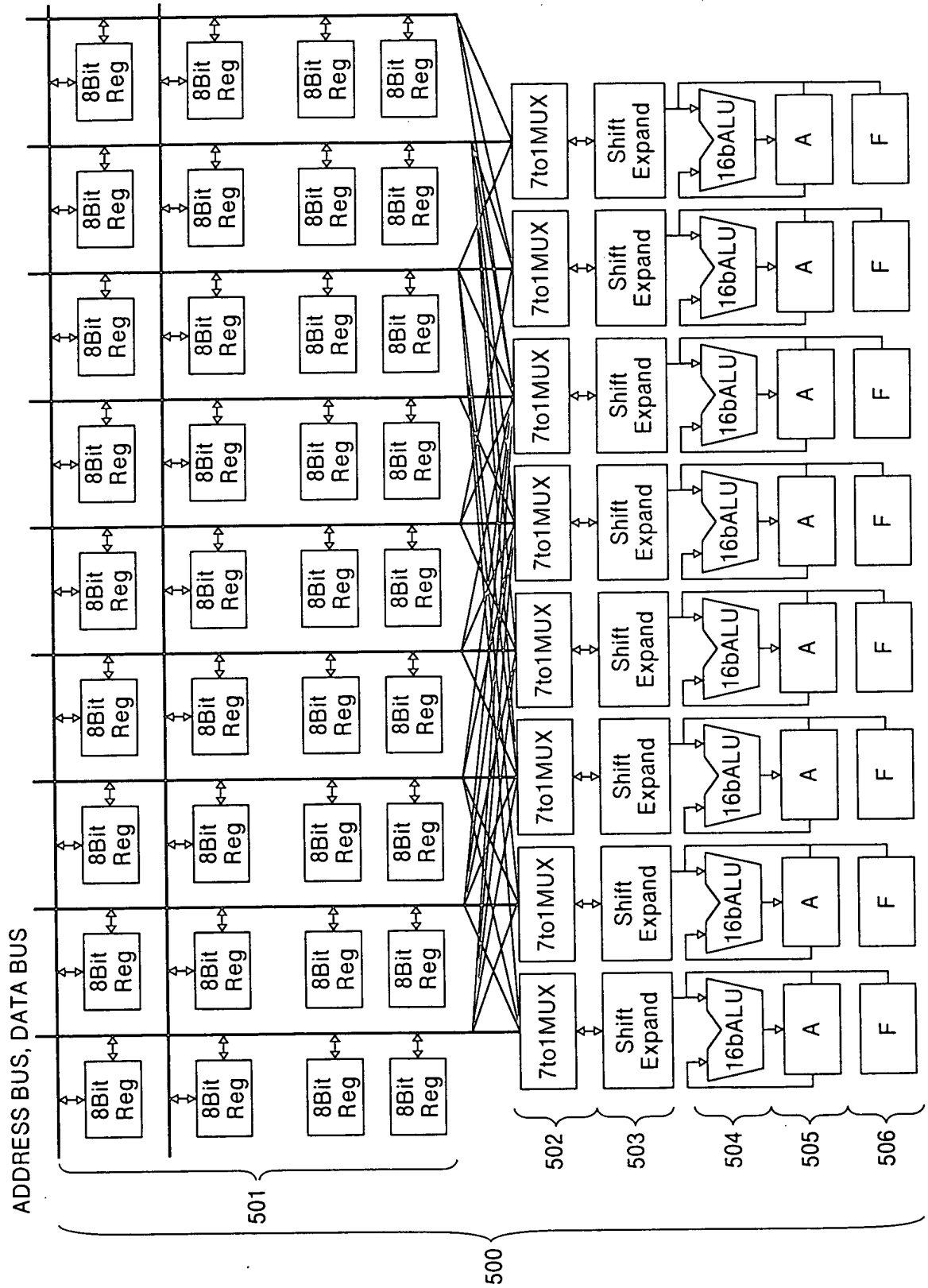


FIG. 5



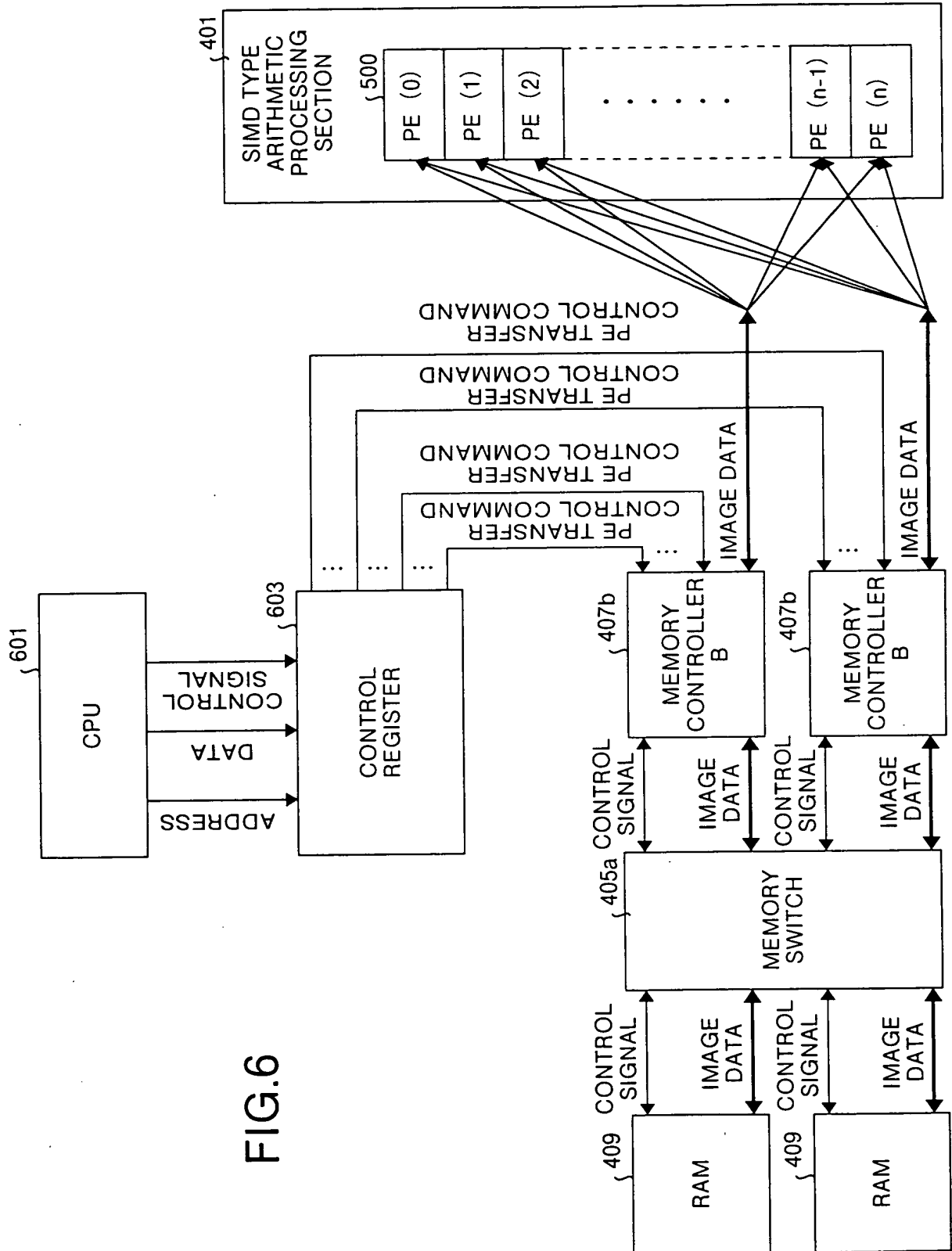


FIG.6

FIG. 6 is a block diagram of a SIMD type arithmetic processing section 401.

FIG.7

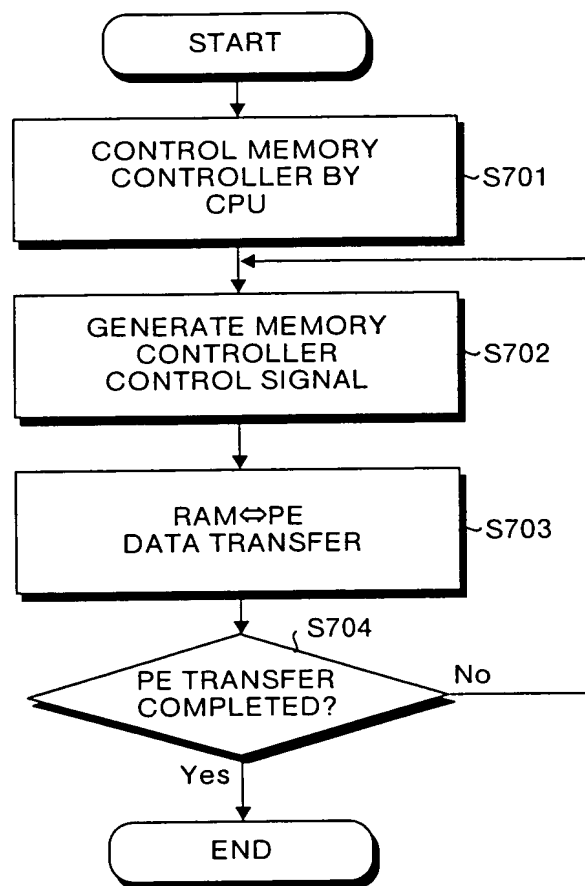
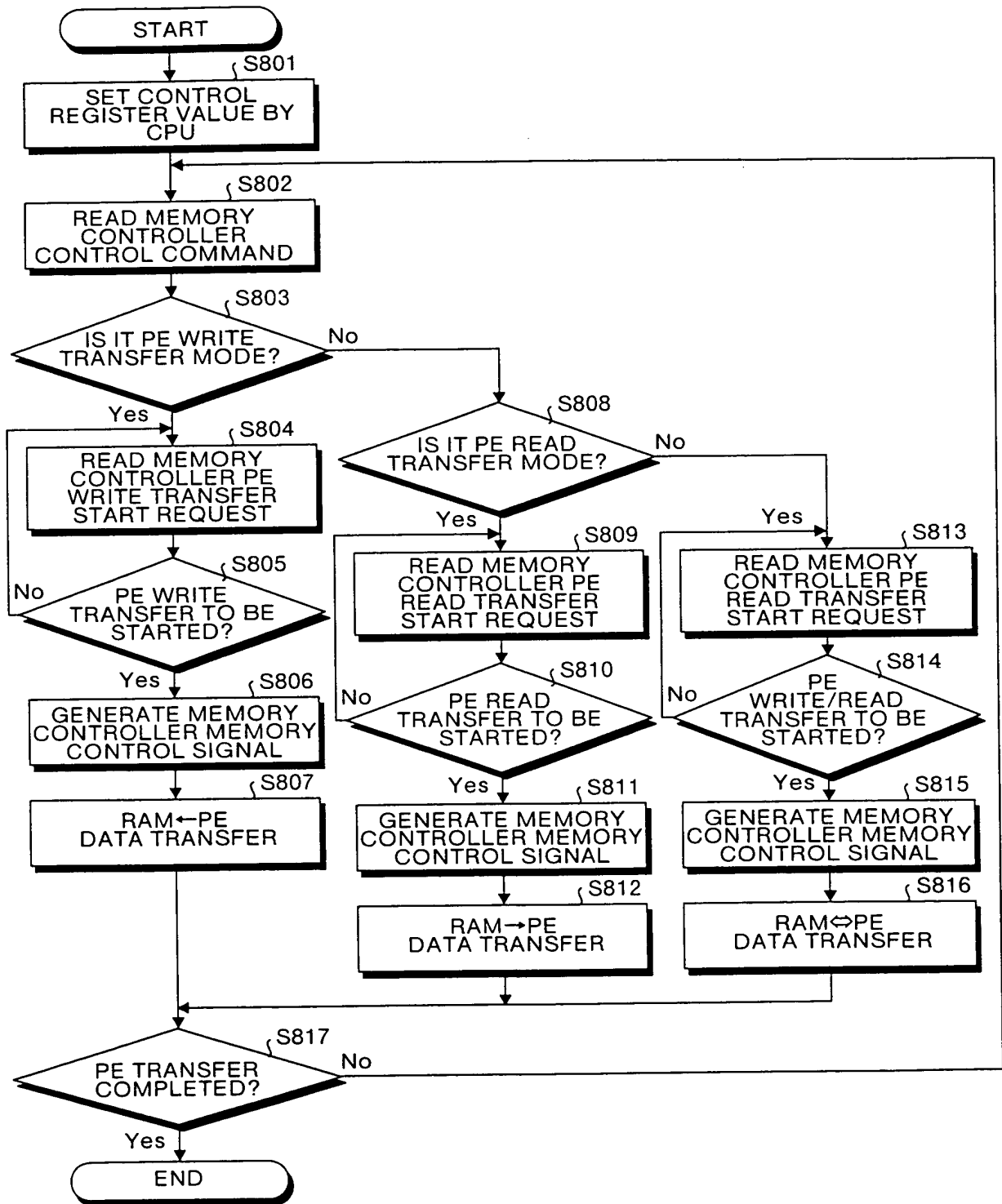


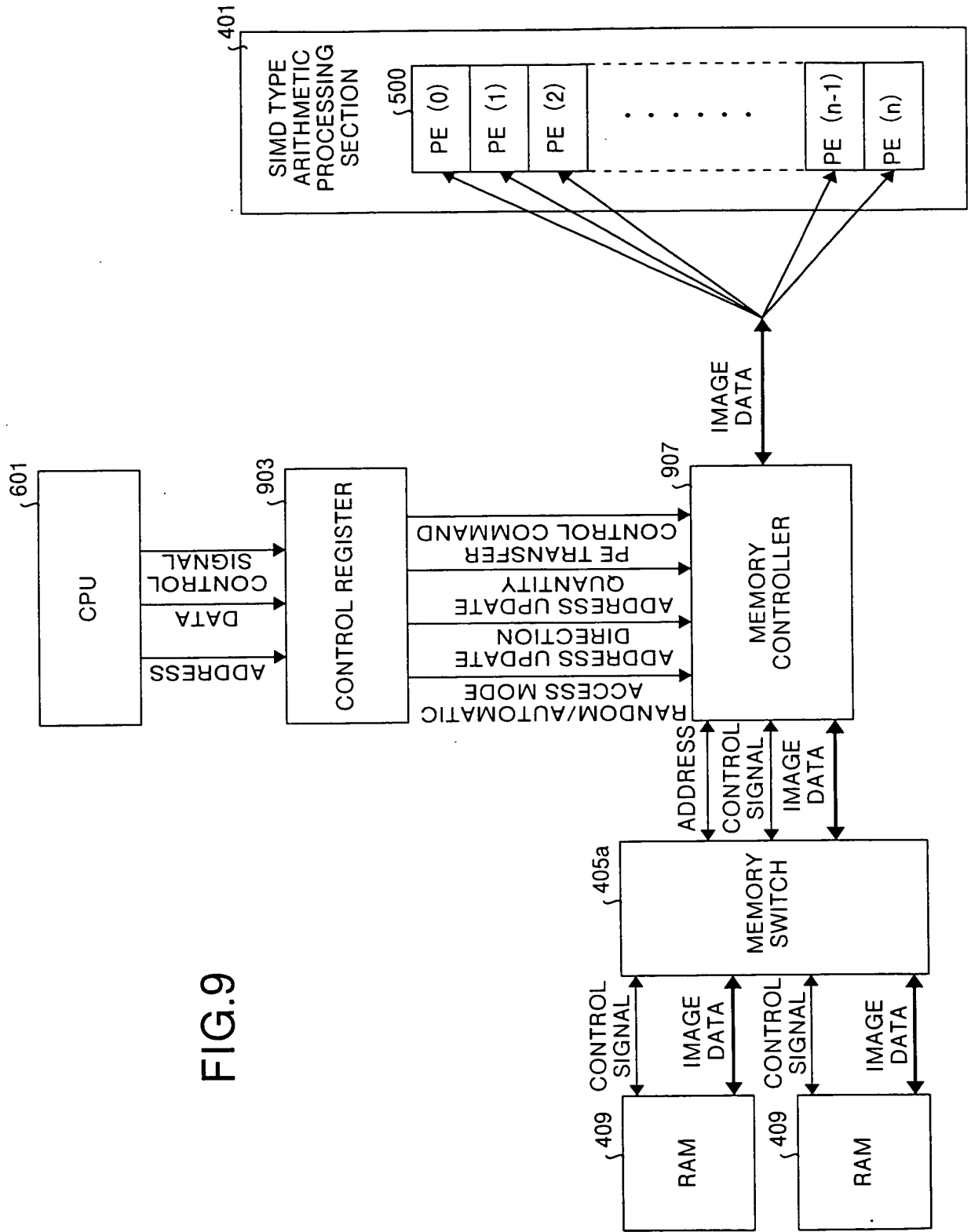
FIG.8





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FIG.9



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FIG.10

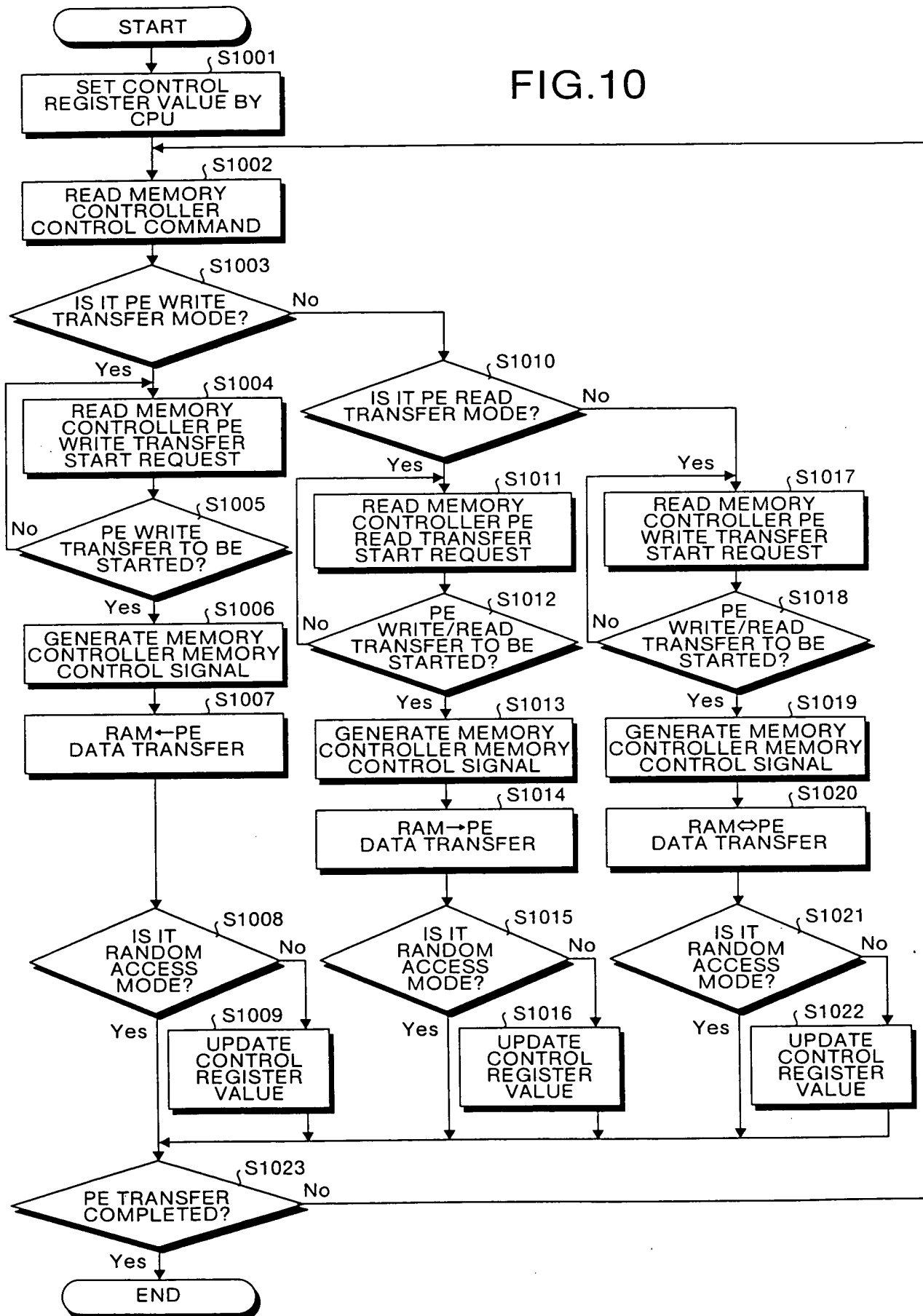


FIG.11

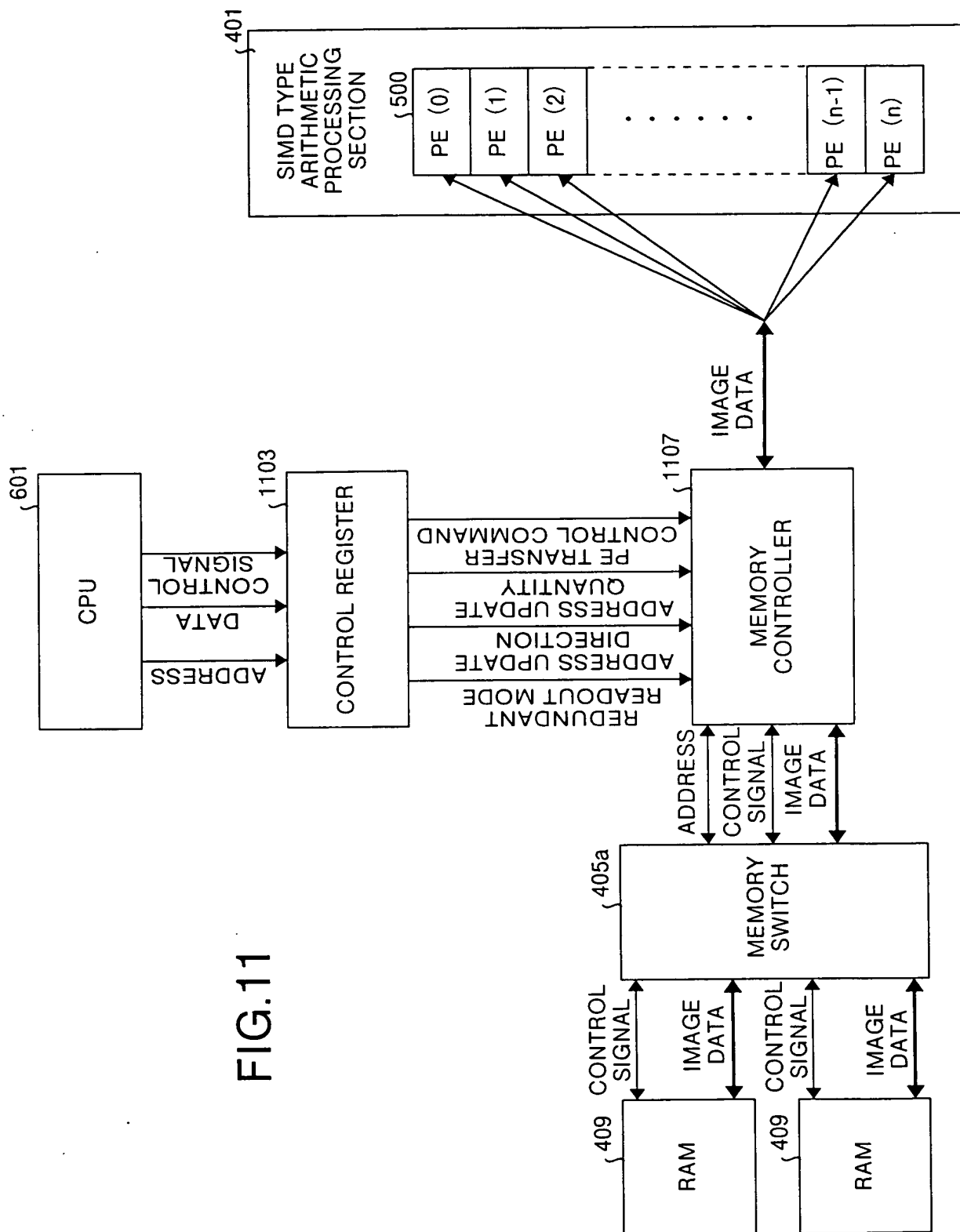
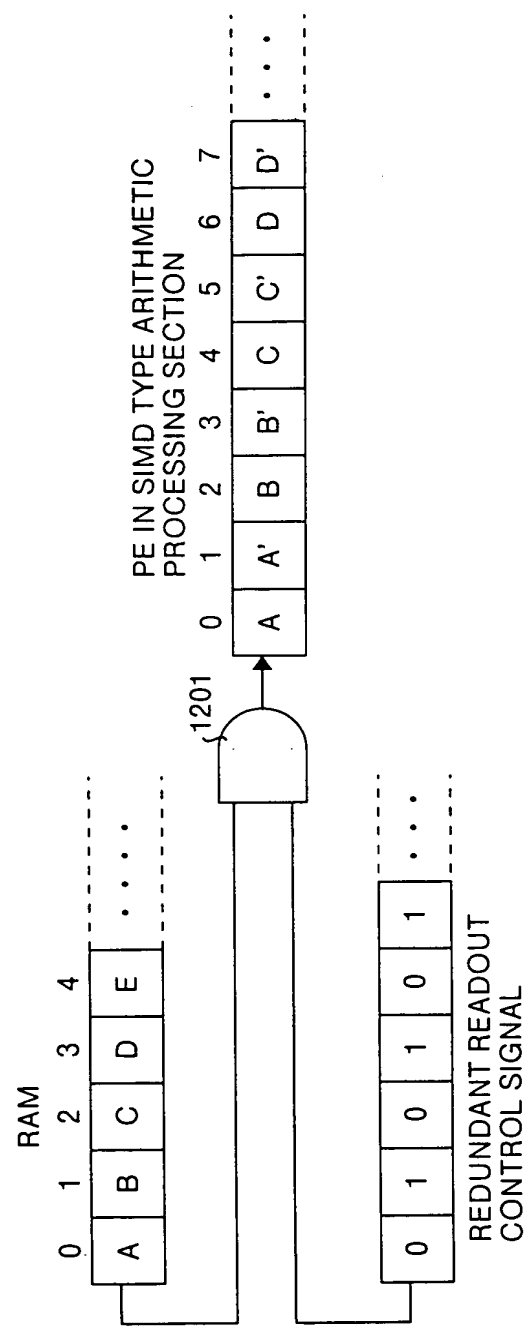


FIG.12



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FIG.13

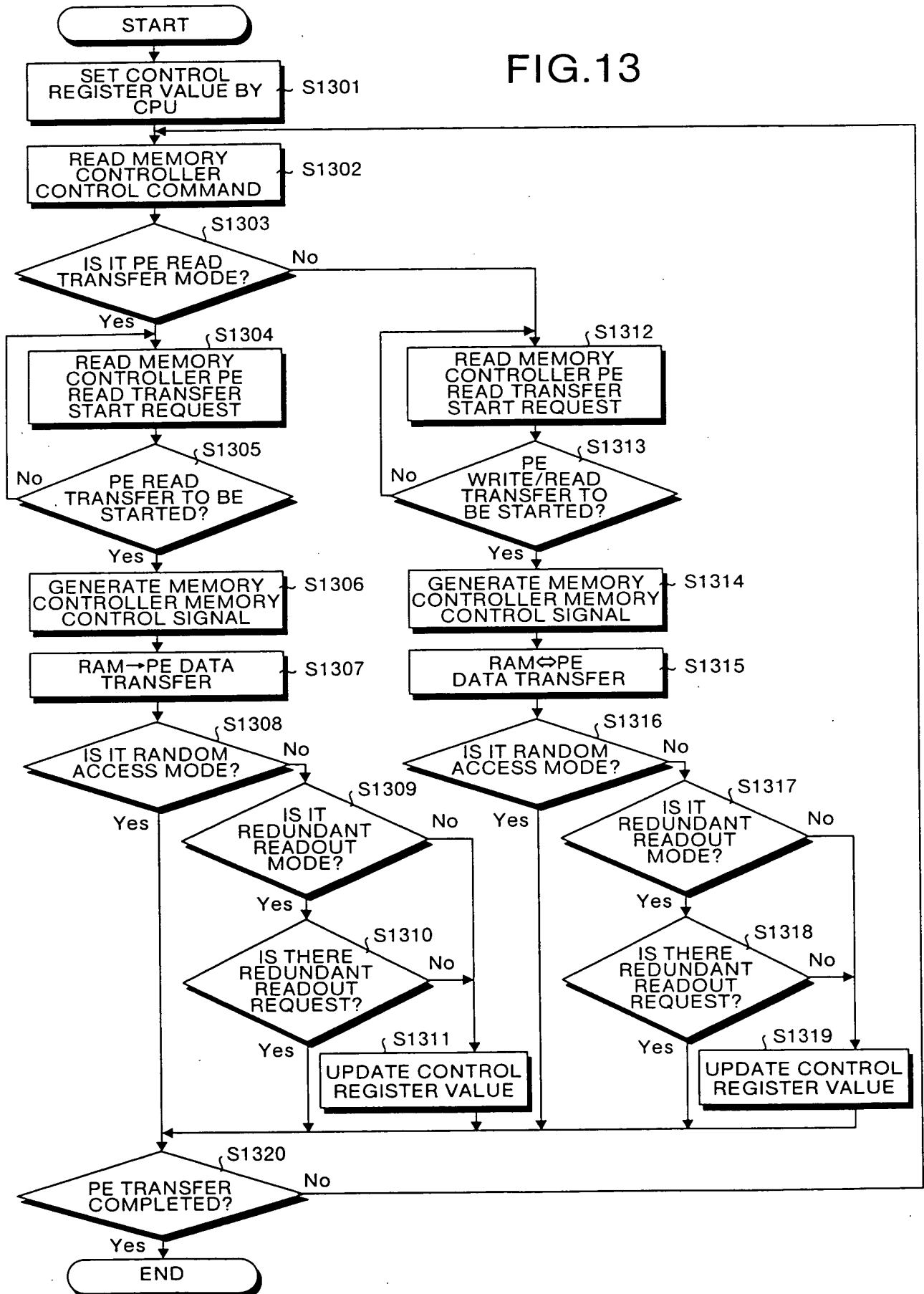


FIG.14

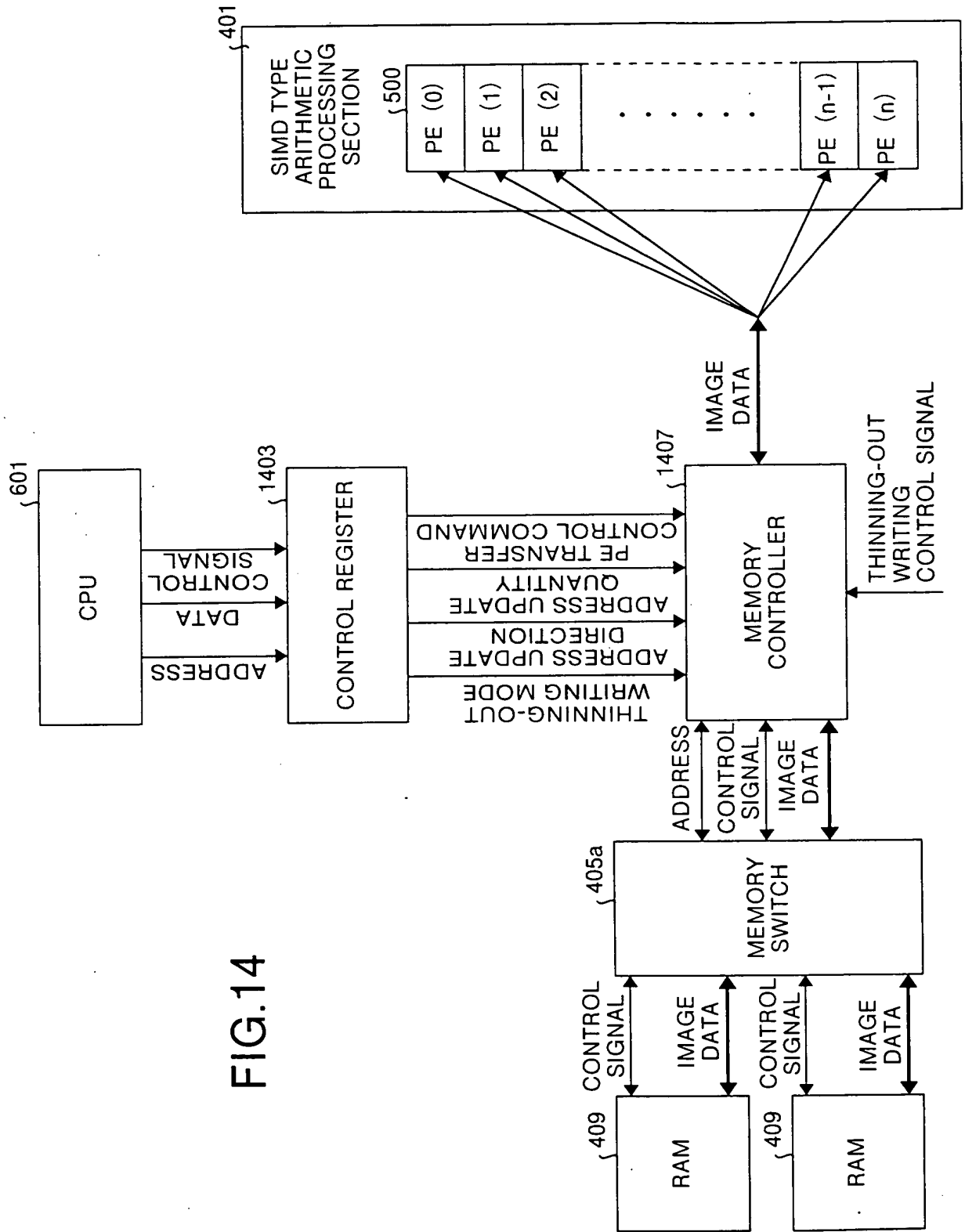


FIG.15

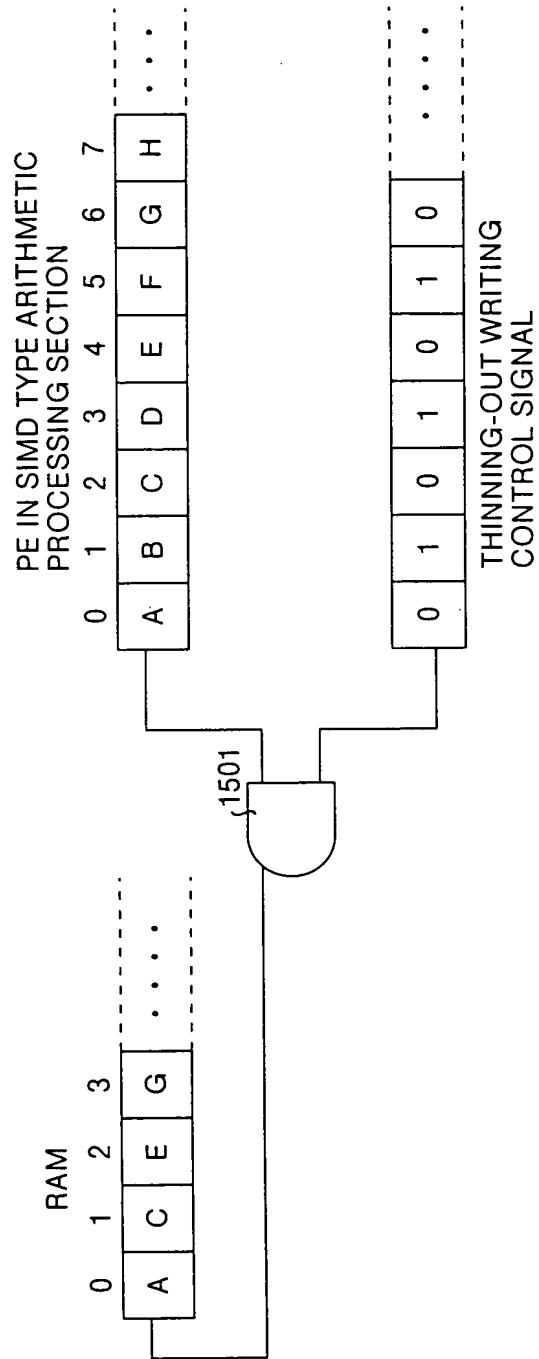


FIG.16

